

# **AT89STK-09 Starter Kit for AT83C26**

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## **User Guide**





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**Section 1**

Introduction .....	1-2
1.1 Acronyms .....	1-2
1.2 Features .....	1-2

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**Section 2**

Hardware .....	2-6
2.1 Power Supply .....	2-6
2.2 Jumper Configuration .....	2-6
2.3 Host Microcontroller Connection .....	2-6
2.4 Test points .....	2-9

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**Section 3**

Quick Start .....	3-12
3.1 Smart cards Insertion .....	3-12
3.2 Layout .....	3-13

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**Section 4**

Schematics & Bill Of Material .....	4-14
4.1 Bill Of Material .....	4-18



# Section 1

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## Introduction

This document helps the user to get started with the AT89STK09 starter kit.

The AT89STK09 is a starter kit for the AT83C26 device. The AT83C26 is a smart card reader interface with 2 DC/DC converters allowing to manage up to 5 smart cards including level shifters. This device needs TWI commands to configure its registers. This device requires a host microcontroller (Smart Card Reader Microcontrollers, ARMs, ...) with one or two ISO7816 Smart Card Interfaces.

Refer to the AT83C26 datasheet for further product information.

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### 1.1 Acronyms

- TWI : Inter Integrated Circuit
- SCIB: Smart Card Interface Bus
- LDO : Low Drop Output (Regulator)

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### 1.2 Features

The AT89STK-09 Board provides the following features:

- AT89STK09 board with 2 ISO7816-3 connectors and 4 SIM/SAM connectors
- 2 Gemplus GemClub Smart cards
- One 20 multicolor wires cable with HE10 connector
- Smart Card Reader IC CDR0M (from may 2005)
- 2 VQFP48 & 2 QFN48 samples
- AT89STK09 user guide document

The AT89STK09 board allows a large number of hardware configurations.

Figure 1-1. AT89STK09 board

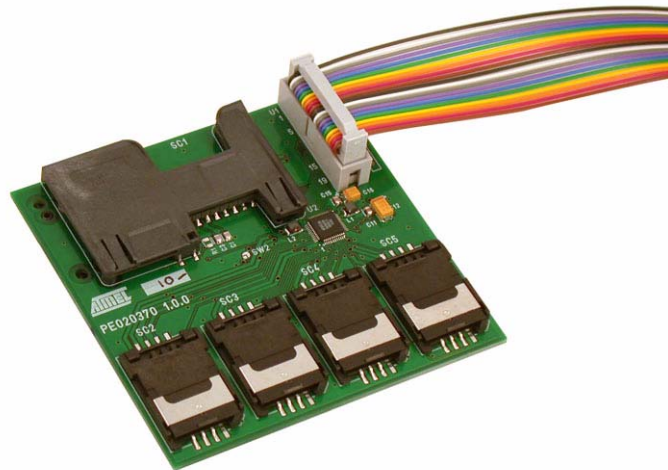
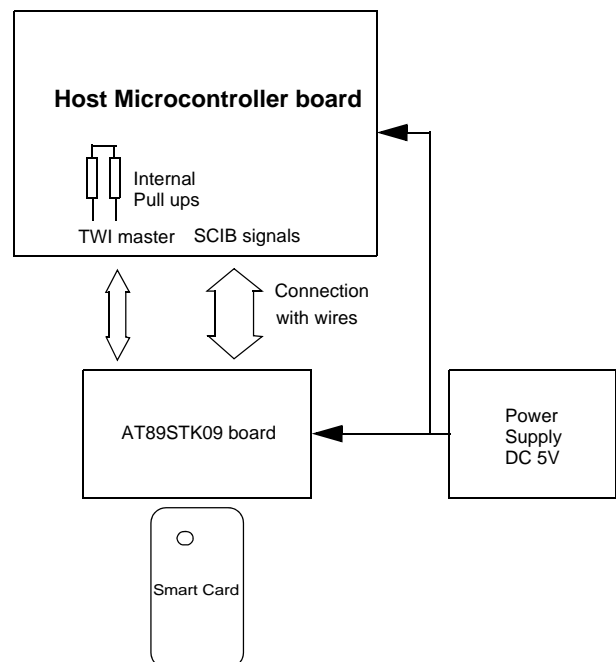


Figure 1-1 describes a possible hardware configuration with:

- Same power supply voltage for AT89STK09 board and host microcontroller board
- TWI master signals from a host microcontroller board
- SCIB signals connected to AT89STK09 board
- Smart card clock from host microcontroller board

Figure 1-2. Configuration Description





## Section 2

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# Hardware

The AT89STK09 requires power supply, clock signal, smart card and TWI commands to run. U1 connector is used to connect the AT83C26 host pins to the host microcontroller. The U1 connector pins are described on Table 2-1.

All passive components needed (inductors, capacitors) are soldered on board.

- 
- 2.1 Power Supply** The host microcontroller signals voltage and the AT83C26 power supply are VCC. The voltage range (VCC) is 3V to 5.5V, the maximum current consumption  $I_{VCC}$  is about 250mA when all DC/DC convertors and LDO are activated. (Conditions :  $V_{CC} = 3V$  60mA DC/DCa, 70mA DC/DCb).
- 
- 2.2 Jumper Configuration** The AT83C26 board has one jumper in order to set complete SC2 configuration. The complete SC2 configuration (Smart Card including CC4 & CC8) is done by SW1 solder pad.
- 
- 2.3 Host Microcontroller Connection** The host microcontroller must provide the signals:
- VCC, VSS : Power supply signals - 3 - 5.5V @ 250mA
  - EVCC can be used when the  $V_{CC}$  host and  $V_{CC}$  are different. Generally the EVCC is connected to VCC.
  - BYPASS is used to activate the low power consumption when connected to the host microcontroller (high level must be supplied) - Generally the BYPASS is connected to the ground.
  - A2, A1 : TWI address - Generally  $A2 = 0$  and  $A1 = 0$  during Reset
  - SDA, SCL : TWI signals to transmit/receive commands/status
- Caution : TWI pull-ups have to be on the host microcontroller board*
- CLK signal is connected to host clock signal
- Caution : The logic inverter (HC04 or other) is recommended if source clock is supplied by output crystal oscillator buffer.*
- $\overline{INT}$  (Interrupt) : This signal must be connected to host interrupt input
  - CRST signal is connected to RST host microcontroller signal
  - CCLK signal is connected to the host smart card interface signal

- IO1, IO2, AUX1 and AUX2 are connected to the host smart card interface signals depending on the configuration chosen. (Refer to the datasheet AT83C26)

**Table 2-1.** U1 Description

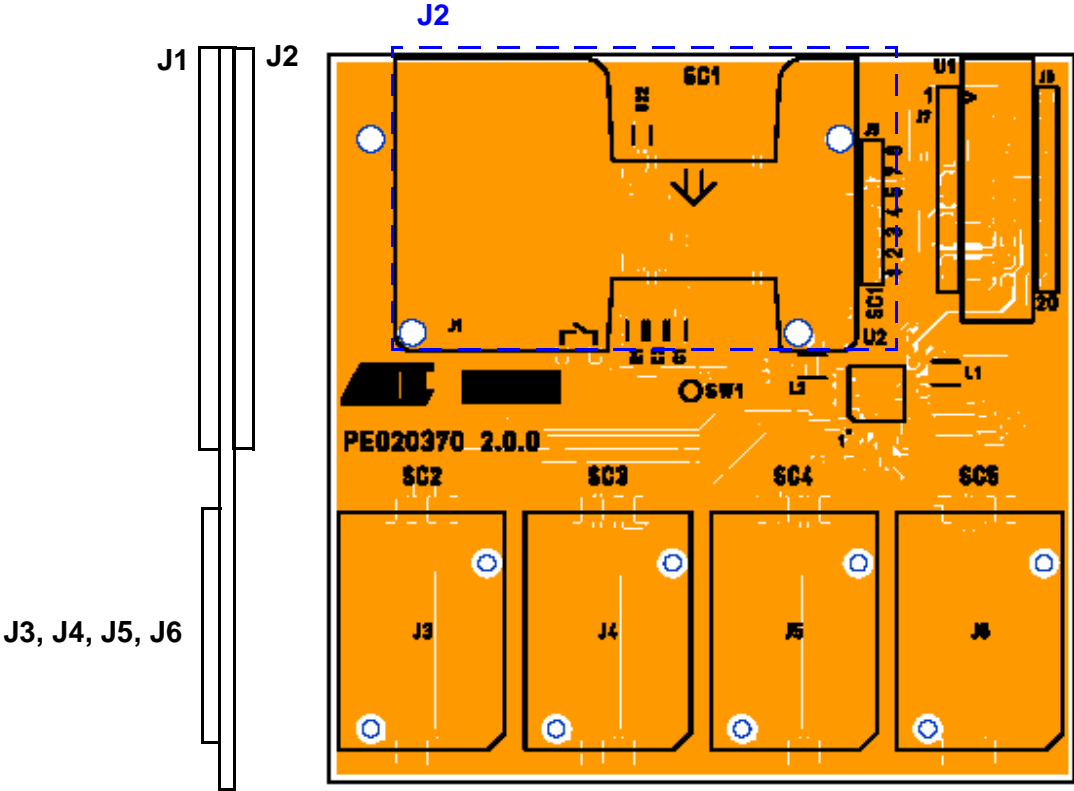
#	Name	Color
1	CCLK	Brown
2	$\overline{\text{INT}}$	Red
3	CRST	Orange
4	Not Connected	Yellow
5	VSS	Green
6	CLK	Blue
7	VSS	Velvet
8	Not Connected	Grey
9	AUX2	Blank
10	$\overline{\text{RESET}}$	Black

#1	Name	Color
11	AUX1	Brown
12	Not Connected	Red
13	IO2	Orange
14	BY PASS	Yellow
15	IO1	Green
16	VSS	Blue
17	SCL	Velvet
18	VCC	Grey
19	SDA	Blank
20	EVCC	Black

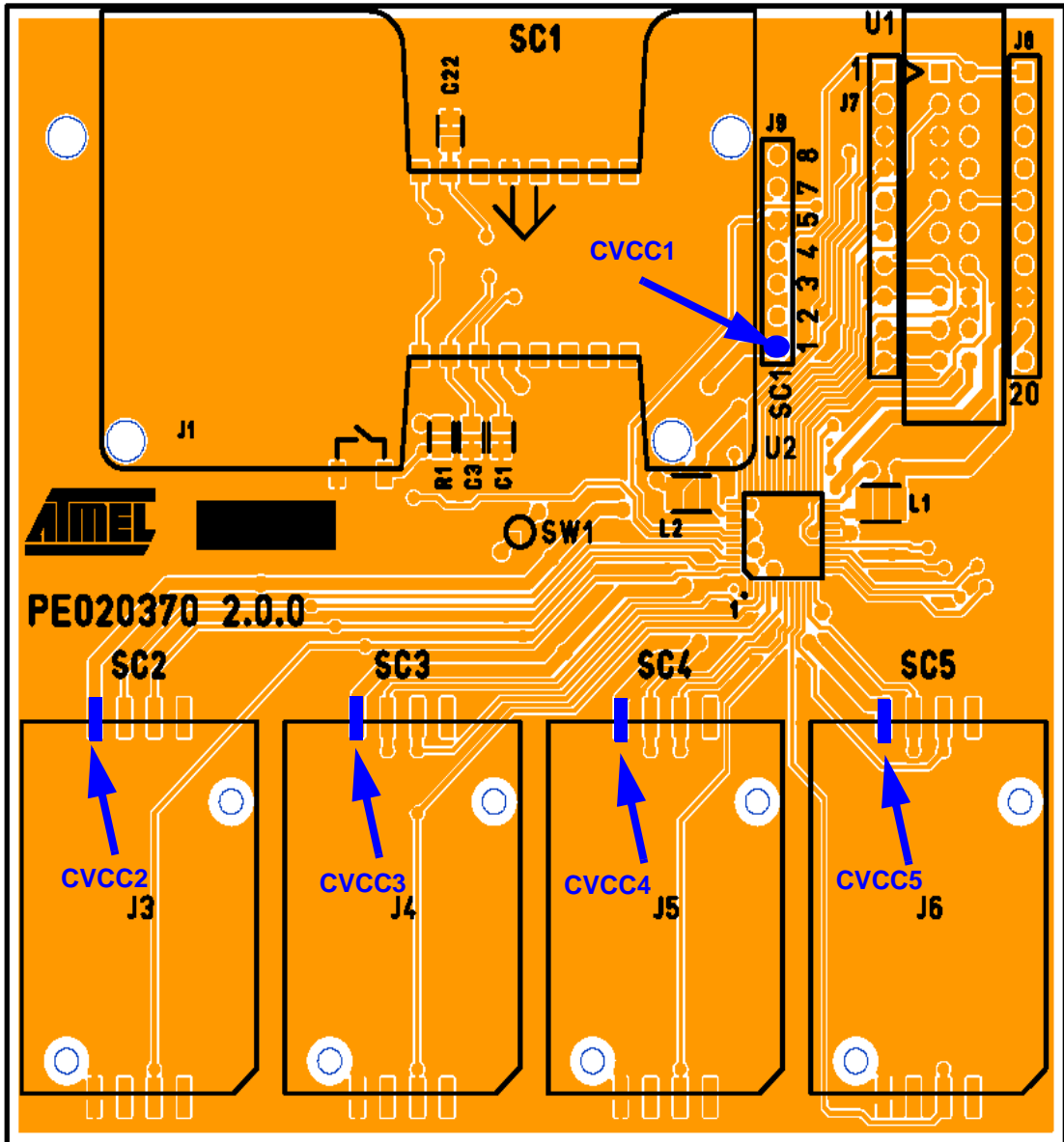
### 2.3.1 CPRES0 & 1 External Pullups

R1(component side), R2 (solder side) can be added if internal CPRES0, CPRES1 pull up are not activated. R1, R2 recommended values are between 27KOhms to 1MOhms. By default, resistors value is 180KOhms

Figure 2-1. AT89STK09 board



## 2.4 Test Points



Caution : Active probe use is recommended to measure all smart card interfaces signals

Signal	
DC/DCB	



## Section 3

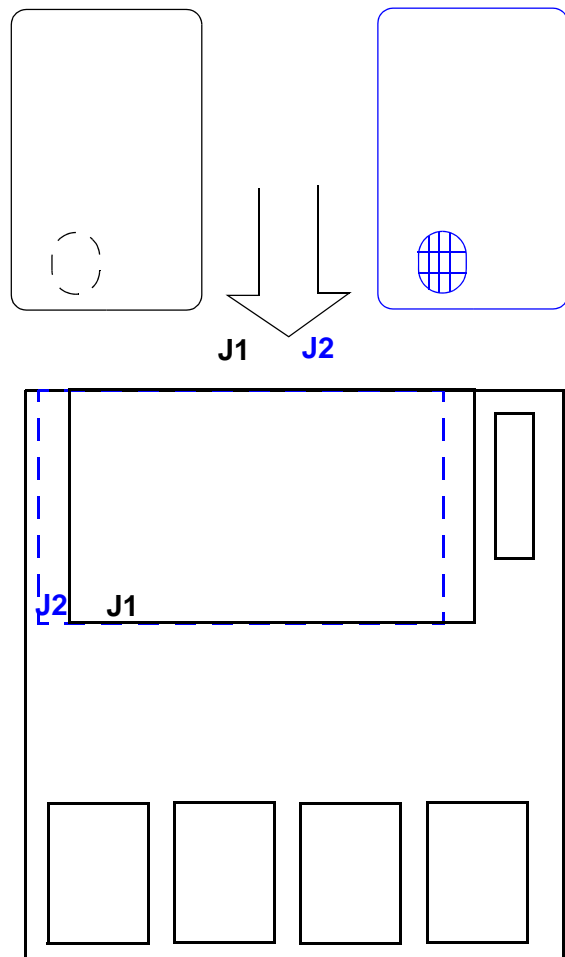
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# Quick Start

### 3.1 Smart cards Insertion

Be careful with the direction of the smart cards in the connector.

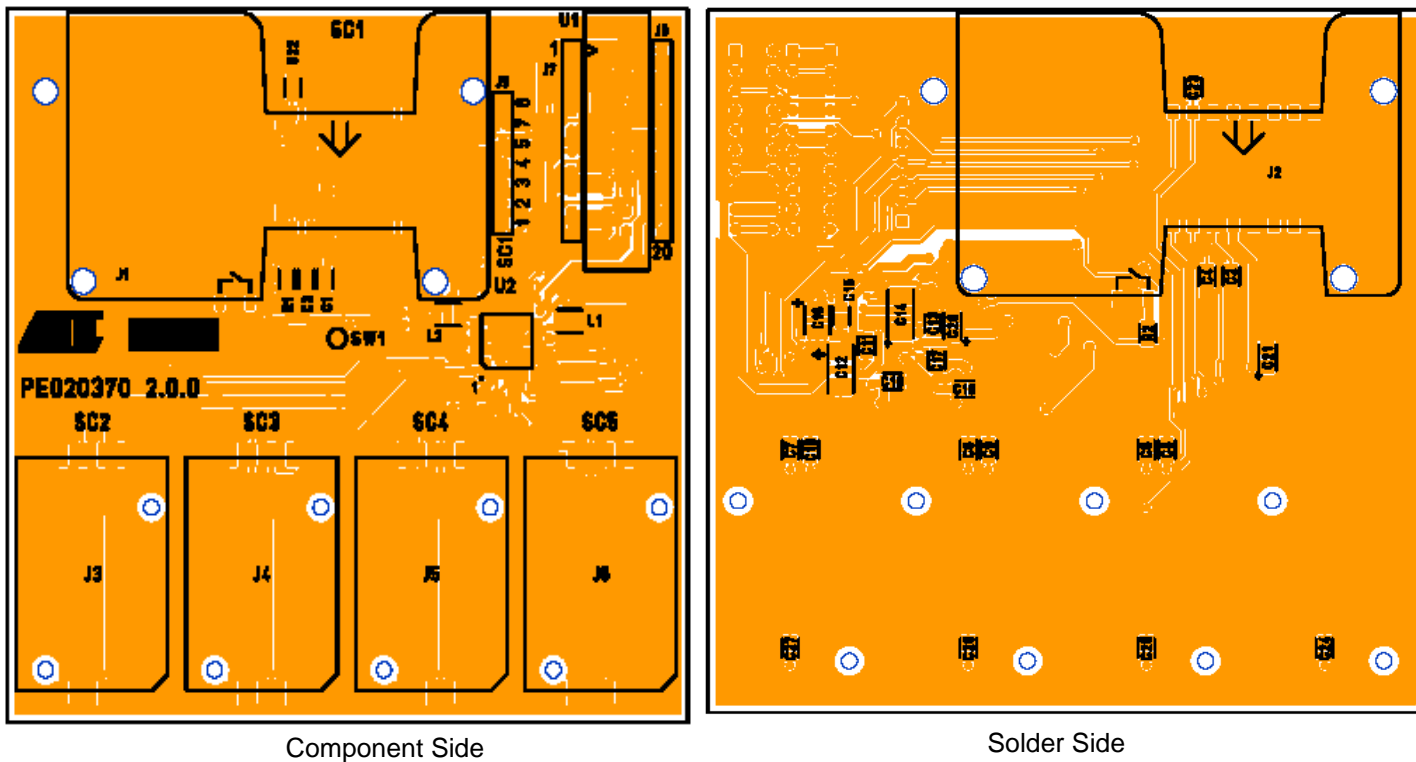
**Figure 3-1.** Smart Card Direction



### 3.2 Layout

The board layout has been optimized in order to reduce the noise, overshoot & undershoot on the smart card interface signals. The board is used to pass EMV certification level 1. Gerber file is available on demand at [cardreader@atmel.com](mailto:cardreader@atmel.com)

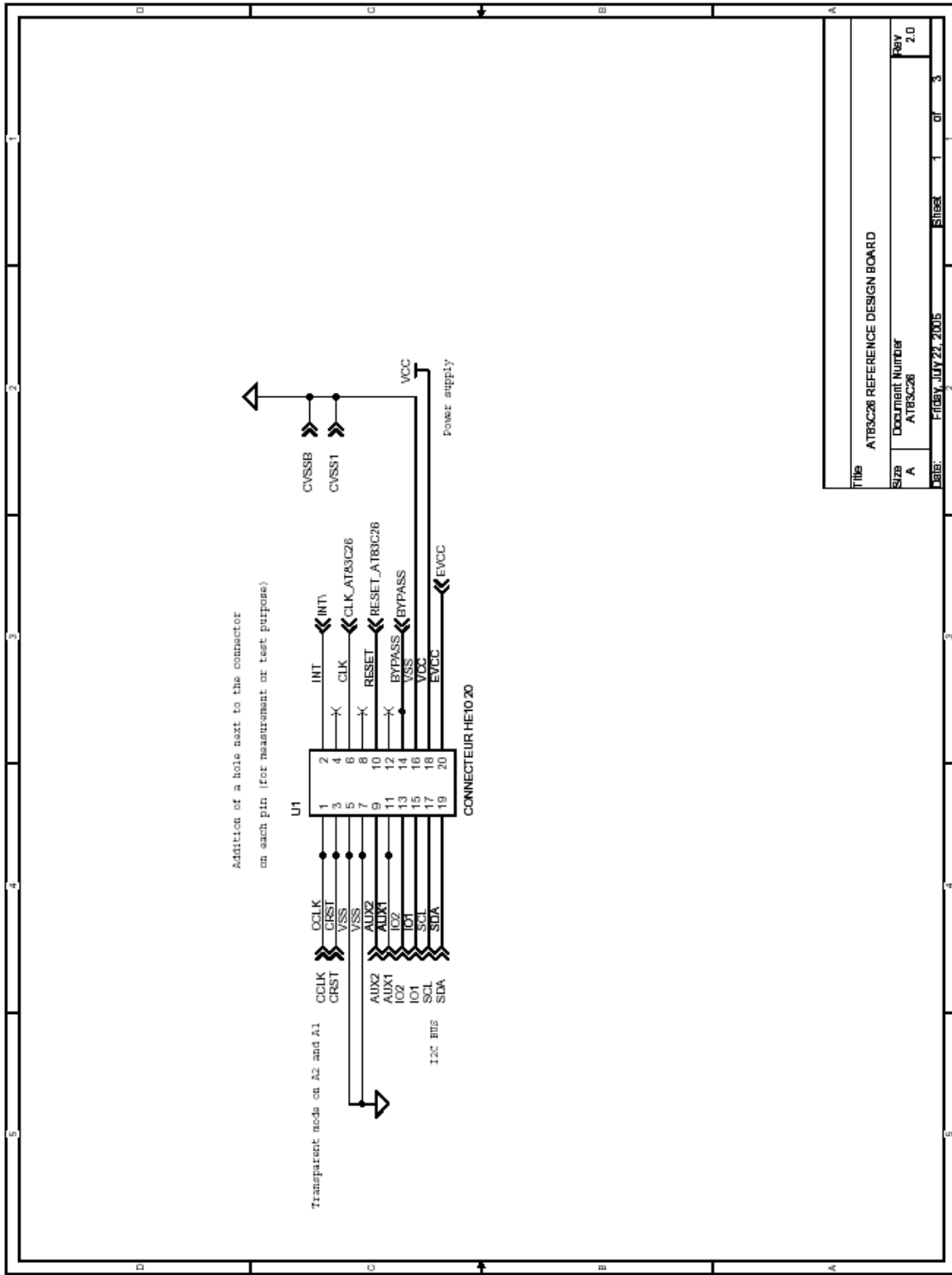
Figure 3-2. Component & Solder Sides



## Section 4

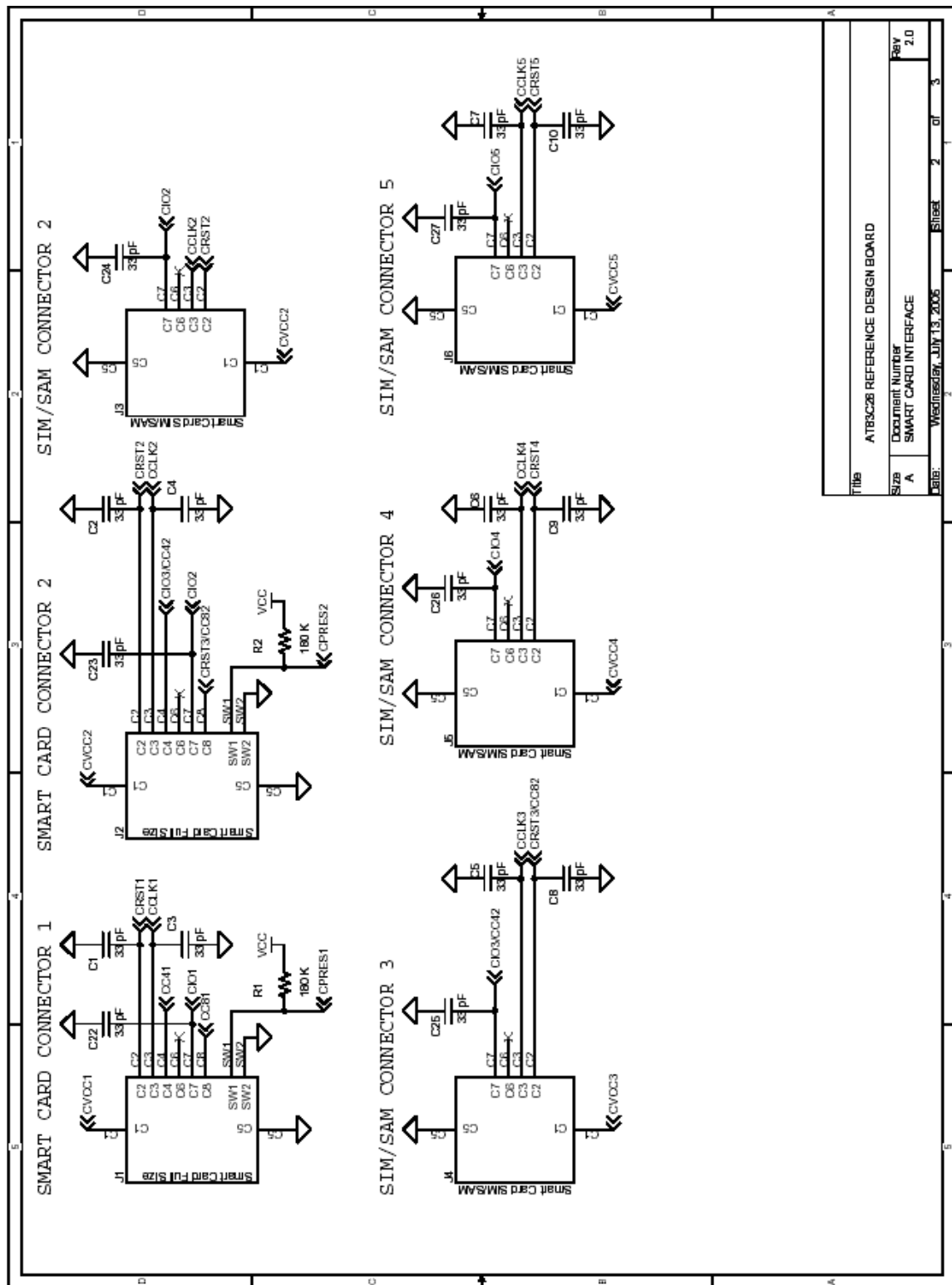
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# Schematics & Bill Of Material



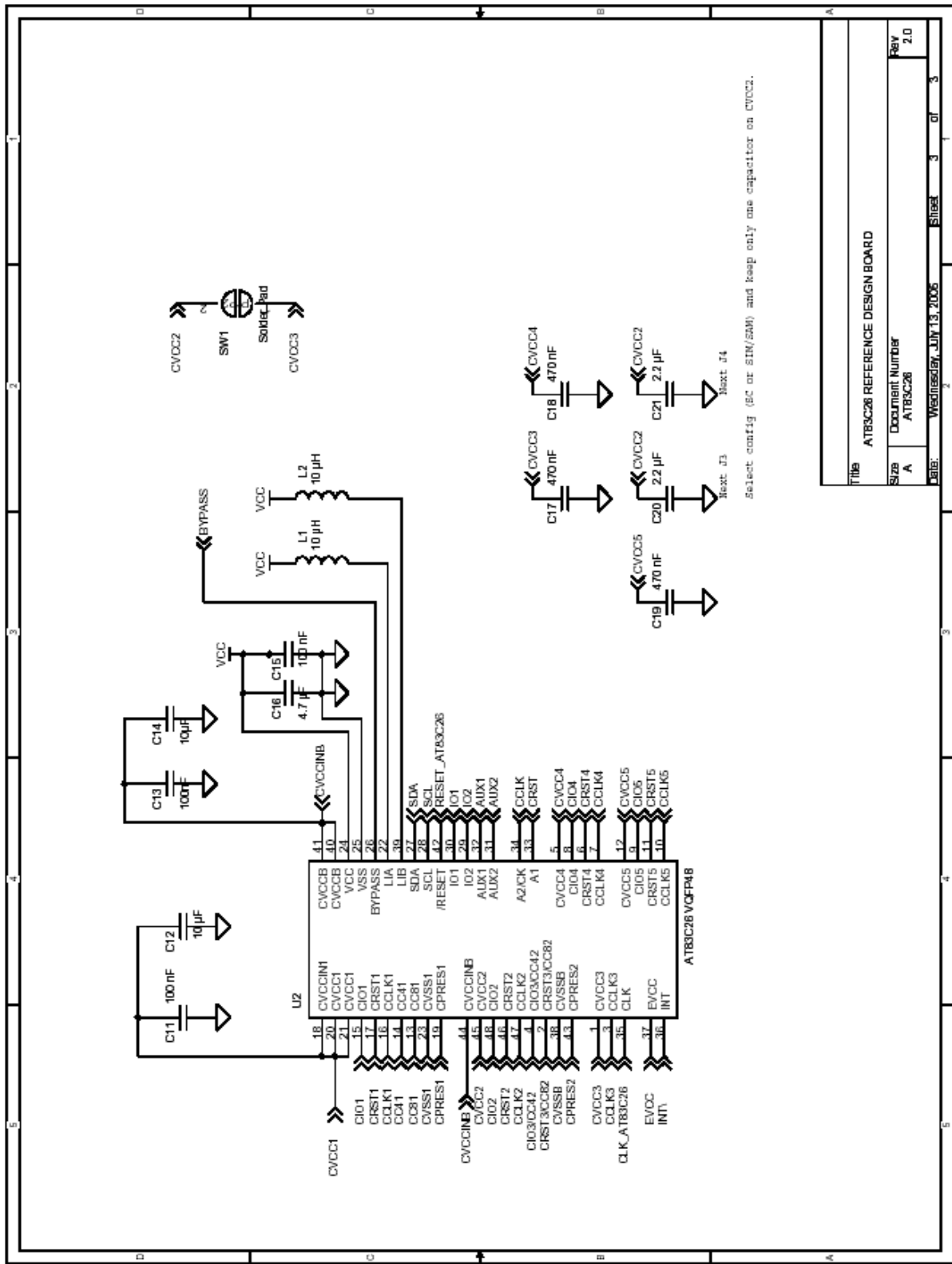
Title		AT89C26 REFERENCE DESIGN BOARD	
Size	Document Number	REV	
A	AT89C26	2.0	
Date:	File	July 22, 2006	Sheet 1 of 3





Title		AT89C26 REFERENCE DESIGN BOARD	
Size		DOCUMENT NUMBER	
A		SMART CARD INTERFACE	
Date		Wednesday, July 13, 2005	
Page		Sheet 2 of 3	
Rev		2.0	





Select config (5C or 81N/50M) and keep only one capacitor on CVCC2.

THIS AT89C26 REFERENCE DESIGN BOARD	
Size	Document Number
A	AT89C26
Date:	Wednesday, July 13, 2005
Sheet	3 of 3
Rev	2.0



## 4.1 Bill Of Material

Reference	Part	Description	Manufacturer Ref
C1,C2,C3,C4,C5,C6, C7, C8, C9, C10, C22, C23, C24, C25, C26, C27	33 pF	50V-5% Ceramic Capacitor	
C11,C13, C15	100 nF	50V-5% Ceramic	
C14	10 $\mu$ F		
C20,C21	2.2 $\mu$ F		
C17,C18,C19	470 nF	16V-10%-Ceramic	
C16	4.7 $\mu$ F		
C12	10 $\mu$ F	16V +/-20%	
R1,R2	180 KOhms	1/16W-5%	
L1,L2	10 $\mu$ H	10% Inductance 300 mA SMD	MURATA LQH32CN100K21L or LQH32CN100K11L or LQH32CN100K01L
U2	AT83C26	VQFP48	



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1150 East Cheyenne Mtn. Blvd.  
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